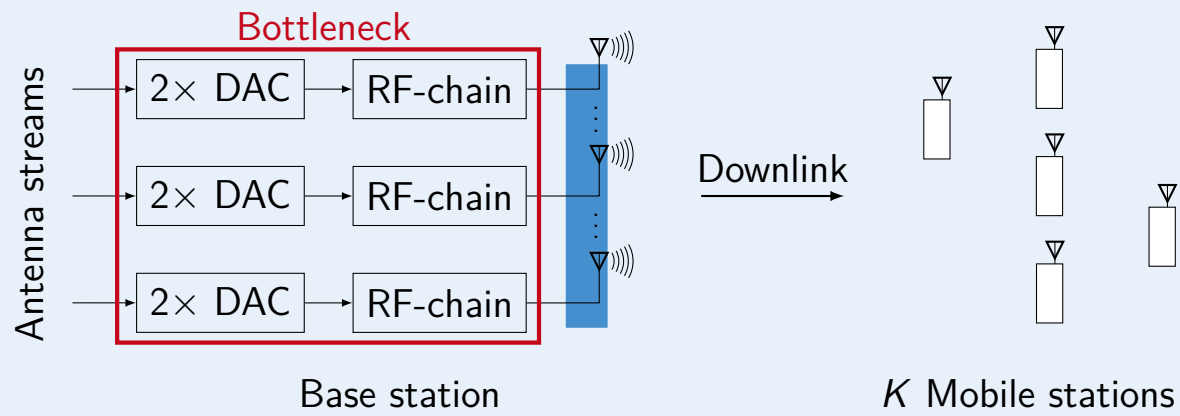


Motivation

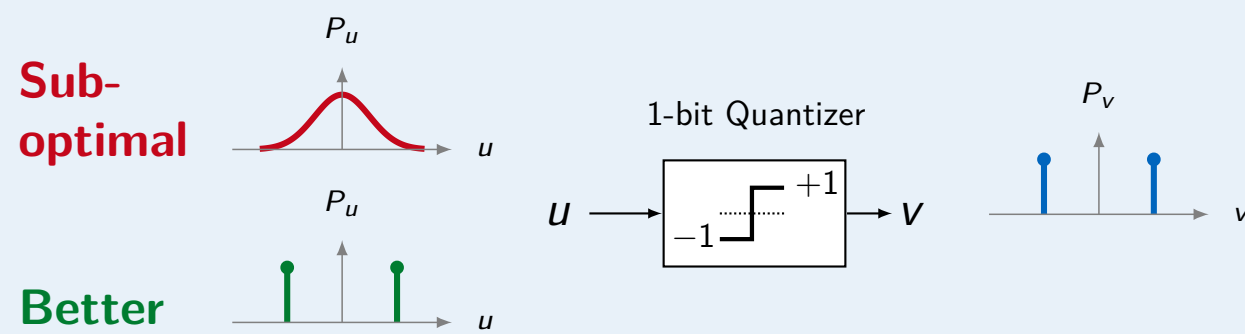
- **Goal:** Low-complexity and power-efficient **massive** MIMO system
- **Bottleneck:** Hardware complexity and power-consumption of RF-chains [1]
 - ⇔ Linearly operated Power Amplifiers (PAs)
 - ⇔ High-resolution Digital-to-Analog Converters (DACs)
- Does **not scale** for a **massive** MIMO system



- **Idea:**
 - 1-bit DACs ⇒ simplifies complexity and efficiency of RF-chains
 - Linear precoding ⇒ Find precoder **only** once per channel coherence time

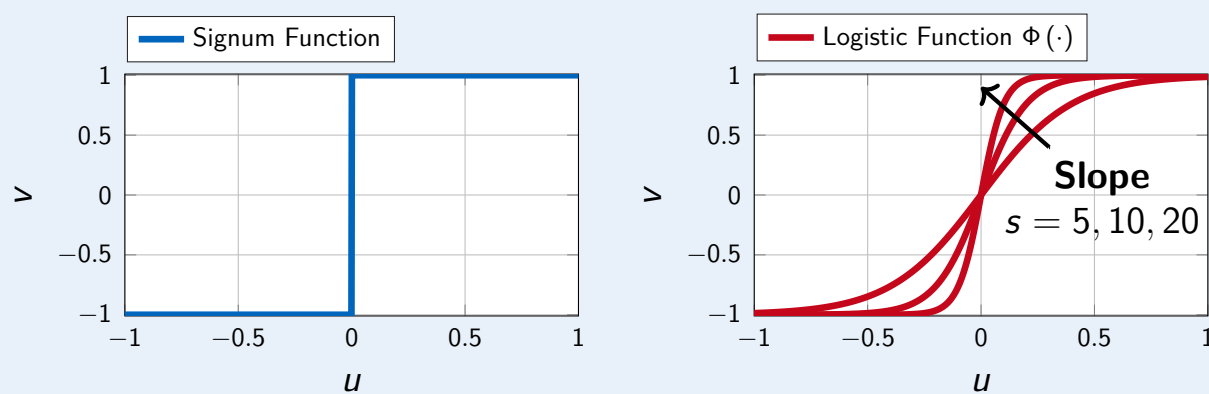
DAC Model (1-bit)

- **State-of-the-art:** Statistical model for quantizer ⇒ Assumes Gaussian u
- Our approach: Model quantizer by **Sigmoid** function

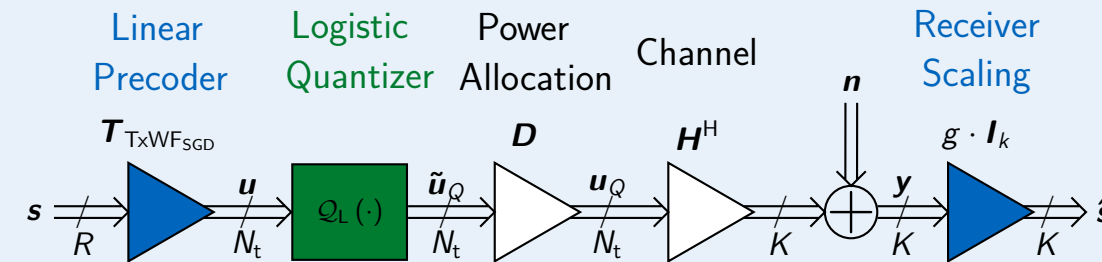


Smooth Approximation of 1-bit DAC

- Approximate Signum function by a Sigmoid function $\Phi(\cdot)$



System Model (flat fading)



- **Of interest:** $\mathbf{T}_{\text{TxWFSGD}}, \mathbf{g}$
- **Logistic quantizer:** $Q_L(\cdot) = \Phi(\Re\{\cdot\}) + j \cdot \Phi(\Im\{\cdot\})$
- Power allocation: *Equal* ($\mathbf{D}_{\text{EQ}} = c \cdot \mathbf{I}_{N_t \times N_t}$), *non-equal* ($\mathbf{D}_{\text{NEQ}} = \text{fct}(\mathbf{T})$)
- TX-Power constraint: $\mathbb{E}[\mathbf{u}_Q^H \mathbf{u}_Q] \leq P_{\text{tx}}$
- Noise: $\mathbf{n} \sim \mathcal{CN}(\mathbf{0}_K, \mathbf{C}_{nn})$

Constrained Optimization Problem

$$\text{MSE: } \epsilon = \frac{1}{N} \cdot \|\mathbf{\Pi} \mathbf{S} - \hat{\mathbf{S}}\|_F^2$$

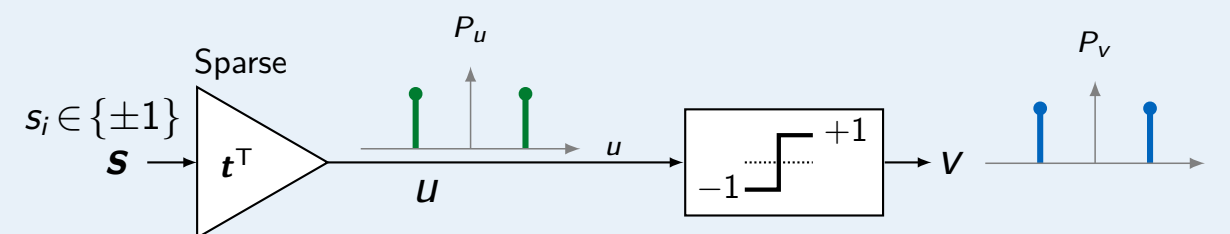
- Data symbols \mathbf{s} from QPSK
- **Sample matrix** $\mathbf{S} \in \mathbb{C}^{R \times N}$: $[\mathbf{s}_1, \dots, \mathbf{s}_N]$, all combinations of \mathbf{s}
- **Receive symbol matrix** $\hat{\mathbf{S}} \in \mathbb{C}^{K \times N}$: $[\hat{\mathbf{s}}_1, \dots, \hat{\mathbf{s}}_N]$
- Superposition matrix $\mathbf{\Pi} \in \mathbb{C}^{K \times R} \Leftrightarrow$ Higher-rank precoding [2]
- Normalization: $1/N$

Finding $\mathbf{T}_{\text{TxWFSGD}}$ and \mathbf{g}

MSE smooth and nonlinear in $\mathbf{T} \Rightarrow$ (Stochastic) Gradient Algorithms

Complexity and Regularization

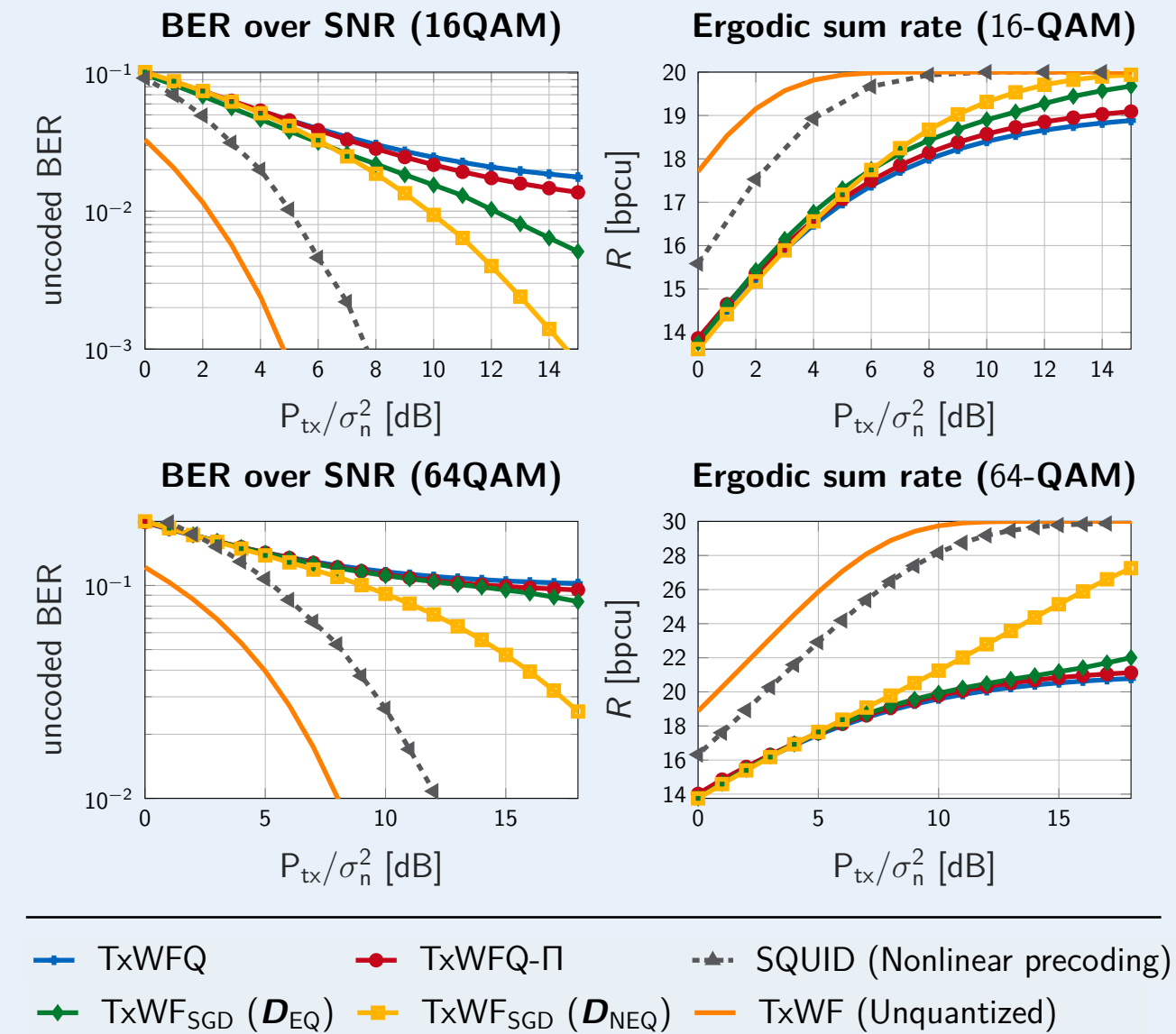
- **Complexity** per step $\mathcal{T} \propto N = |\mathcal{A}_{\text{rx}}|^K$
- **Receiver alphabet** $|\mathcal{A}_{\text{rx}}| = 4$ (QPSK), 16 (16-QAM), ...
- ⇒ Reduce \mathbf{S} and use regularization \nleftrightarrow **Overfitting**



Interpretation of Regularization

- **Sparsity:** l_1 -regularization of \mathbf{T} [3] \Rightarrow reduces quantization error
- **Quantization noise model:** l_2 -regularization of \mathbf{T} [4]

Numerical results (5 users, 80 TX-antennas, perfect CSI)



Conclusion

- **Improved rate and BER** compared to state-of-the-art linear precoders
- TxWFSGD (\mathbf{D}_{EQ}) **outperforms** TxWFSGD and TxWFSGD- Π
 - ⇒ Hardware simplification due to equal power allocation
- TxWFSGD (\mathbf{D}_{NEQ}) **reduces gap** to nonlinear precoding methods
- **Linear precoding** (once per channel coherence time) \Leftrightarrow **Nonlinear precoding** (for each transmit vector)
- **Further avenues:** Imperfect CSI and complexity comparison

References

- [1] C. Mollén, *High-end performance with low-end hardware: Analysis of massive mimo base station transceivers*. Linköping University Electronic Press, 2019, vol. 1896.
- [2] O. De Candido, H. Jedda, A. Mezghani, A. L. Swindlehurst, and J. A. Nossek, "Reconsidering linear transmit signal processing in 1-bit quantized multi-user mimo systems," *IEEE Transactions on Wireless Communications*, vol. 18, no. 1, pp. 254–267, Jan 2019.
- [3] R. Tibshirani, "Regression shrinkage and selection via the lasso," *Journal of the Royal Statistical Society. Series B (Methodological)*, vol. 58, no. 1, pp. 267–288, 1996.
- [4] A. Mezghani, R. Ghai, and J. A. Nossek, "Transmit processing with low resolution d/a-converters," in *2009 16th IEEE International Conference on Electronics, Circuits and Systems - (ICECS 2009)*, Dec 2009, pp. 683–686.