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Summary of Case Studies in Focus - Part II

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- Part II -

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Abstract

This paper is the continuation of a survey of case studies that are have been carried out using the methodology Focus. After a short introduction to Focus, the essential aspects of the case studies are shortly described, and the relevant publications are referenced for further reading.

Contents

1 FOCUS -
A Design Methodology for Distributed Systems

Following the FOCUS methodology, a system development process goes through several levels of abstraction starting at an abstract requirement specification and going down to a concrete implementation description. During the *requirement phase* a first and possibly very abstract formalization of an informal description is developed. It will be used as the basis for the following phases. In this step, specifications can be formalized as either trace or functional specifications. The transition between these paradigms described by the FOCUS methodology is formally sound and correctness preserving. In FOCUS, the specifications are based on denotational semantics which map specifications onto sets of traces or sets of stream processing functions. Choosing stream processing functions, each function describes a possible behavior.

In the *design phase*, that follows the requirement phase, an essential part of the system development is carried out by developing the architecture of the system and refining it up to the required level of granularity. For the development of the specifications during the design phase, paradigms like functional and relational specifications as well as several specification styles like "Assumption/Commitment" or equational specifications are supplied using an uniform functional semantics. Due to the specific natures of these variants they can be used tailor-made for the solution of specific problems. During the last step, the *implementation phase*, the specifications, developed during the design phase, are transformed into one of the supplied implementation languages.

During the formal development of distributed systems the intended level of granularity is reached by stepwise refinement of the system. For this purpose FOCUS offers a powerful compositional refinement concept as well as refinement calculi, including behavioral, interface and structural refinement. On the whole, FOCUS offers a mathematically and conceptually sound framework supporting a complete system development from the abstract requirement specification to the implementation.

Since the semantic foundations of FOCUS including its development techniques are already explored in depth, the emphasis for further developments lies on a better applicability of the methodology, especially by system developers less experienced in formal methods. For that purpose, additional wide-spread description techniques, (semi-)automatic and schematic proof support and tailor-made methods for specific application fields have to be offered. Several techniques for describing and specifiying systems (like tables, state or system diagrams, MSC-like event traces, the "Assumption/Commitment" style) were successfully integrated in the methodology. With AUTOFOCUS tool-support for a system-development is already available, giving future case studies a new quality by offering appropriate editors, consistency checks, code generation and even simulation.

Case studies are an important and stimulating work for testing Focus in different application areas. Focus will be further improved, using the experience gained from the case studies described in this paper and future studies to come. We invite the reader to use Focus for the specification and development of distributed systems, and contact us for additional support using the Focus methodology.

Focus is described in detail in

> Manfred Broy, Ketil Stølen.
> Focus *on System Development*.
> Manuscript, to appear, Springer, 1997.

and also in

> M. Broy, F. Dederichs, M. Fuchs, T.F. Gritzner, R. Weber.
> *The Design of Distributed Systems - An Introduction to* Focus.
> SFB-Report 342/2-2/92 A, Technische Universität München, 1993.

More information about AutoFocus can be found in

> F. Huber, B. Schätz, A. Schmidt, K. Spies.
> AutoFocus - *A Tool for Distributed Systems Specification*.
> in: Bengt Jonsson, Joachim Parrow (editor): "Proceedings FTRTFT'96
> - Formal Techniques in Real-Time and Fault-Tolerant Systems", 467-470,
> LNCS 1135, Springer, 1996.

2   The Case Studies

We present a summary of the case studies done with Focus. For every case study the description is split into three parts:

(i)   a brief problem description,

(ii) comments on the formal treatment with Focus, and

(iii) references to the respective paper(s).

The enumeration of the studies is continued from the predecessing paper:

> M. Broy, M. Fuchs, T.F. Gritzner, B. Schätz, K. Spies, K. Stølen.
> *Summary of Case Studies in* Focus –
> *A Design Method for Distributed Systems*.
> SFB-Report 342/13/94 A, Technische Universität München, 1994.

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2.22 Communication Busses

(i) The FOCUS development methodology is applied to the development of a hard-ware bus as a communication medium for hardware components. A requirement specification of the bus interface with the definition of conflicts and resolution strategies is given. The initial requirement is refined into hardware-close spec-ifications of bus driver devices and signal lines using formal refinement steps. Futhermore, two variant techniques for conflict resolution are informally intro-duced and formally specified.

(ii) The emphasis of the case study lies on the treatment of hardware oriented devel-opment, using schematic and "Assumption/Commitment" based specifications of bit-level circuits. The case study uses a special instantiation of the FOCUS development methodolgy especially suited for hardware oriented design, using appropriate assumptions about the communication behavior (infinite traces, fixed delays, signal-orientation). Specifications are given using an "Assump-tion/Commitment" scheme. Different forms of refinement steps are carried out (structural refinement, interface refinement). Each refinement step is formally verified using A/C-rules and predicate logic.

(iii) Roberto della Mura.
Spezifikation von Busstrukturen - Eine Fallstudie in FOCUS.
Master's Thesis, Technische Universität München, 1994.

2.23 A Radio Control Module

(i) In this Master's thesis a specification for a radio control module is developed. This module controls the operating modes of a radio. It reacts to a sequence of input commands by sending output commands to the various lower-level com-ponents of the radio. As this work was done in cooperation with an industrial partner, this work emphasizes the use of tables to offer intuitive specifications to users not familiar with formal specifications. One of the aims of the work was to test the capabilties of formal methods in an industrial environment. Another aspect is the motivation and application of data- and interface-refinement rules in the design process.

(ii) The case study covers the design level of FOCUS. A first, abstract specification of the radio control module undergoes structural, data and interface refinement yielding components close to an already existing implementation of the mod-ule. The design begins with a very abstract specification: a single component receives commands on one channel, and outputs control commands on another channel. The exact structure of the control commands must not yet defined. In a structural refinement step, this component is then split into one processing and one memory component. Using data refinement, the commands sent to the radio's lower-level components are then described in more detail; finally, interface refinement is applied to split the control module's single output chan-nel into two channels corresponding to two internal busses of the radio. The

thesis presents all needed refinement rules, and all refinement steps are formally proven.

(iii) Max Breitling.
Formale Entwicklung eines Funkgerätesteuermoduls.
Master's thesis, Technische Universität München, 1995.

2.24   A Modulo-N-Counter

(i) The use of functional system specifications and refinement in the formal development of hardware is illustrated using the example of a asynchronous Modulo N-counter device. The development includes modular specification, refinement and verification. The development is started at an intuitive requirements specification and is refined into a nontrivial concrete bit-level implementation.

(ii) The case study covers the complete range of the formal FOCUS development methodolgy. The development includes modular specification, refinement and verification. The refinement steps comprise behavioral, structural and interface refinement. The emphasis of this study is laid upon the modelling at different levels of abstraction and the verification conditions obtained by the refinement relations between these versions.

(iii) Maximilian Fuchs.
Formal Design of a Modulo-N Counter.
SFB-Report SFB 342/06/95 A, Technische Universität München, April 1995.

2.25   Abracadabra Service and Protocol

(i) The ISO/OSI basic reference model contains a variety of services and protocols in order to guarantee a secure data transfer. A well known service is the Abradadabra service which is realized by the Abracadabra protocol. The Abracadabra service guarantees a connection-oriented, symmetrical and reliable data exchange over an unreliable medium. In the Master's thesis, based on some ISO publications of informal and formal descriptions of the Abracadabra-example using techniques like LOTOS, SDL and Estelle, a formal specification of the Abracadabra service and protocol is developed within the FOCUS framework.

(ii) The informal description of the Abracadabra service and protocol is transformed in a formal specification using pulse-driven functions. The main emphasis lies on the demonstration of the suitability of the "Assumption/Commitment" style in modelling especially the connection and disconnection of components. In addition this Master's thesis shows that the development of a detailed formal specification discovers some lack of clarity in the informal description. Also some mistakes in the SDL specification, referenced by the ISO, are discovered and discussed. Modelling the Abracadabra protocoll was the first larger case study using the time dependent and pulse-driven FOCUS-semantics.

(iii) Christine Klein.
Spezifikation eines Dienstes und Protokolls in FOCUS
Master's Thesis, Technische Universität München, 1995

2.26   A Lift Controller

(i) Two different versions of a lift controller are covered in this case study. In the first version only one elevator cabin is considered, whereas for the second version the system is expanded to n lifts. In both examples there are m floors, each of them with two buttons: one for signaling the request to go up and one to go down (except on the top and the bottom floor). Each lift has a set of m buttons for the user to signal a request to transport him to a certain floor. In order to serve such requests — both internal (i.e. signaled from inside the cabin) and external (i.e. signaled from one of the floors) requests — a lift may perform a sequence of discrete actions like "up by one floor", "down by one floor" and "stop", with a "stop" in between each change of direction. The strategy used for these examples is the same as in example 2.7 (of part I) "A Lift Controller".

This case study covers the first two development levels while emphasing on the requirement specification.

(ii) The informal descriptions of both examples are translated into a formal trace specification, which is global, i.e. the system is not yet divided into seperate components like elevators or floors. Because of the little differences between the two specifications only the single lift system is transformed into a local trace specification. In order to do this it is necessary to incorporate a model of time. Two possible distribution are then considered: one with the lift and the m floors as the components, and the other one which has an additional component to control the communication between the lift and the floors. The later one is finally converted to a functional specification, which is the first step on the design level.

(iii) Jürgen Rudolph.
Entwicklung einer Liftsteuerung – Fallstudie in FOCUS.
Master's Thesis, Technische Universität München, 1995.

2.27   The Production Cell

(i) This study extends the work in 2.18, where a design specification of a production cell is developed, for the usage of mechanical proof support. The production cell works as described in 2.18: It consists of a press for metal plates and several components for handling the plates automatically. The work pieces are moved into the cell by a conveyor belt. An elevating table lifts them into an appropriate grasp position for a robot. The robot both moves plates waiting at the table into the press and also puts the pressed plates on a deposit belt.

This work concentrates on the development of some component specifications within a theorem prover environment. Starting with an abstract specification

for the production cell, specifications for the subcomponents of the elevating table are developed by applying hierarchical refinement steps. Each refinement step is proved to be correct using the generic theorem prover *Isabelle*.

(ii) The emphasis of this case study is to support the proofs involved in the design phase by the logic HOLCF (Higher Order Logic of Computable Functions). HOLCF is itself based on the generic theorem prover *Isabelle*. The network description language ANDL is used to formalize FOCUS-agents in HOLCF. Also, FOCUS' refinement rules for "Assumption/Commitment" specifications are both formalized and proved to be correct in *Isabelle*/HOLCF. The proofs follow closely those in 2.18. However, while in 2.18 they are done as paper proofs, they are carried out completely within *Isabelle's* logical framework here. Especially the treatment of "Assumption/Commitment" specifications in *Isabelle* is of special interest.

(iii) (a) Robert Sandner.
   Unterstützung von Strukturverfeinerungen in FOCUS *durch Isabelle – Verifikation einer Fertigungszelle.*
   Master's Thesis, Technische Universität München, 1996.
   (b) Robert Sandner, Olaf Müller.
   Theorem Prover Support for the Refinement of Stream Processing Functions.
   in: Ed Brinksma (editor): "Tools and Algorithms for the Construction and Analysis of Systems", LNCS 1217, Springer 1997.

2.28   A Buffer of Length One

(i) This case study illustrates the use of the functional system specifications and their refinement in the development of system components like it's explained in the FOCUS-methodology.

The paper presents the specification of a simple one element buffer as a component that can store one data element and return it upon request; of a fair loose one element buffer that may fail in storing data elements or in serving requests. It is fair in the sense that it will not fail forever on repeated attempts. The driver composed with the fair loose buffer behaves like a one element buffer.

The real time one element buffer is a one element buffer that operates in a discrete time frame. The real time fair loose one element buffer operates in a discrete time frame too. It indicates success by a positive acknowledgement within a fixed amount of time. The real time driver operates in a dircrete time frame. Composed with the real time fair loose buffer it behaves like the real time buffer.

(ii) The small example is used to demonstrate functional formalisms for the specification, refinement and verification of system components. The emphasis is put on the usefulness of functional formalisms in the development process and on the achieved modularity of the system descriptions and the development process.

The one element buffer, the loose buffer end the driver are each presented in three specifications using and demonstrating three specification styles: equational specifications, the "Assumption/Commitment" specification format and state-based specifications.

The system composed of the driver and the loose buffer is specified and the verification that this system is a refinement of the buffer is presented.

The specification of the real time components are presented and it is shown that these components are refinements of the components without time.

Finally the verification that the composition of the real time driver and loose buffer is a refinement of the real time buffer is given and it is shown that this verification can be concluded from the refinement steps concerning the timed components.

(iii) Manfred Broy.
Specification and Refinement of a buffer length one.
in: M. Broy (editor), "Deductive Program Design", 273-304, Springer, 1996, NATO ASI Series, Vol. 152.

2.29   Banking System

(i) The banking system consists of a network of banks interacting with each other and with the environment. Banks can be dynamically founded or liquidated. Each bank maintains a set of private accounts. Accounts can also be dynamically opened or deleted. A given amount of money can be deposited on an account, withdrawn from an account or transfered form one account to another account. Consistency checks should assure that money is withdrawn only if available on the corresponding account and transfered only to existing accounts. Time constrains should assure that given requests are processed in given time. This example shows the typical structure of a distributed information system as it usually occurs in practical applications: local conventional entities interact by exchanging messages.

(ii) Formally the following aspects had to be mastered:

(1) Mobility: interaction between the system components dynamically changes the system-configuration. Privacy preserving properties should control the amount of interference.

(2) Recursion: both banks and accounts can be dynamically created.

(3) Timing: requests are to be processed in a given amount of time.

(4) State: the interplay between classical data modeling aspects and interaction.

The formalism used to master these aspects is based on the model for mobile networks (also see the case study 2.33).

(iii) Available as slides only via WWW under

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http://www4.informatik.tu-muenchen.de/focus
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2.30 Video on Demand

(i) This article argues that formal techniques are indeed useful for practical application, but they should be put to indirect use. To demonstrate the approach, a formal semantics is defined for two forms of popular graphical description formalisms. Based on the formal definition, "safe" development steps and their graphical counterparts are introduced. This yields a graphical development method which relies on precise formal foundations.

(ii) To demonstrate the approach, two pragmatic graphical description techniques, taken from the field of telecommunication, are analyzed regarding their information content and their application in the process of specification development; as a result these techniques are formally defined using trace semantics and a clausal representation scheme. In a next step, refinement schemes for these graphical description techniques are introduced. Their refinement character is verified using their underlying formal semantics defined in the first step. The transformation schemes are expressed in a way suitable for a tool supported development. Together with a guide line for the application of the description techniques and the schemes, thus a tool supported development method for protocol oriented system requirement design is introduced.

Finally, possible further enhancements of the formally based development of technical systems using user oriented description techniques are discussed. The mentioned points include relaxations of the restrictive transformtion based development.

(iii) Manfred Broy, Heinrich Hußmann, Bernhard Schätz.
Graphical Development of Consistent System Specification.
in: M.-C. Gaudel, J. Woodcock (editors), "FME'96: Industrial Benefit and Advances in Formal Methods", 248-267, LNCS 1051, Springer, 1996.

2.31 Remote Procedure Call (using relations)

(i) A memory is specified and decomposed into subcomponents. Firstly, the overall behavior of the memory is specified. Both a reliable and an unreliable version is characterized. Secondly, the unreliable memory is decomposed into three component specifications, namely the specification of the reliable memory, and specifications of a RPC component and a server. The correctness of this decomposition is verified. Thirdly, the specification of the RPC component is decomposed into a lossy RPC component and a server. These two components communicate in accordance with a protocol based on timeouts. Also the correctness of this decomposition is verified.

(ii) A relational specification technique is used. Specifications are expressed in two different formats. The time-independent format is used to describe components whose behavior is time-independent. If time constraints are to be imposed, the time-dependent format is used. The emphasis is put on finding the right balance

between readability and brevity. In particular, nondeterministic behavior is captured in terms of prophecies. The proofs are not formal.

(iii) Ketil Stølen.
Using Relations on Streams to Solve the RPC-Memory Specification Problem.
in: M. Broy, S. Merz, K. Spies (editors), "Formal Systems Specification – The RPC-Memory Specification Case Study", 477-520, LNCS 1169, Springer 1996.

2.32 Remote Procedure Call (functional solution)

(i) see the RPC-description of case study 2.31.

(ii) A functional specification of the syntactic interface and the black box behavoir of the unreliable and the reliable memory component and the RPC-compontent is given. Additionally the clerk for driving the RPC component is specified. These components are separately specified in a modular way. A short discussion of these specifications is presented and their composition into a distributed system of interacting components in a modular way is shown. The proof that the specification of the composed system fulfills again the requirement specification of the reliable memory component is given. Finally a timed version of the RPC component and of a clerk component and their composition is presented.

(iii) Manfred Broy.
A Functional Solution to the RPC-Memory Specification Problem.
in: M. Broy, S. Merz, K. Spies (editors), "Formal Systems Specification – The RPC-Memory Specification Case Study", 183-211, LNCS 1169, Springer 1996.

2.33 Modelling Mobile and Dynamic Systems –
A Simple Batchsystem

(i) The basic semantics of FOCUS does not support the modelling of mobile and dynamic systems. Mobile systems covers interaction between components which may change their communication partners during a system run. Dynamic systems allow the creation of new components and the deletion of existing components during the life of the network. A simple example for dynamic, mobile systems is represented by the batch system.

In its initial configuration, the batch system consists of one master component receiving orders from a user in the environment. The orders are distributed to some submaster components which are created by the master component. For each order (task) a submaster itself creates slave components each handling one task. Due to a given capacity of the submaster only a limited number of slaves can be created. If the maximum number of slaves is created, the master component is informed by the submaster. Receiving a new order by the environment the master component creates the next new submaster.

(ii) This case study focuses on the development of a method for the specification of dynamic, mobile systems. First the semantic model for mobile, dynamic sys-

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tems is introduced with regard to users who are versed in the semantic model of Focus and interested in learning its extension for mobile and dynamic systems. Then, specifying the batch system, it is demonstrated how the specification is developed in a systematic and step-wise way. Using pulse-driven function and a constructive and operational specification style with recursive function equations, guidelines and pattern-like formulas are introduced allowing users to specify mobile, dynamic behaviour in a uniform and schematic way.

(iii) (a) Ursula Hinkel, Katharina Spies.
    Anleitung zur Spezifikation von mobilen, dynamischen Focus-*Netzen.*
    SFB-Report 342/16/96 A, Technische Universität München, 1996.

  (b) Ursula Hinkel, Katharina Spies.
    Spezifikationsmethodik für mobile, dynamische Focus-*Netze*
    GI/ITG-Fachgespräch 1997, Berlin.
    Adam Wolisz, Ina Schieferdecker, Axel Rennoch (Hrsg.)
    GMD-Studie Nr. 315, GMD-Forschungszentrum, 1997.

2.34   Secure Systems

(i) As part of the development of a methodology covering the formal analysis of system security, an authentic and available server component is being developed. Starting from a system specification which can be shown not to be authentic, security mechanisms based on simple cryptographic authentication protocols are introduced. Security analysis works out the subtle differences between two protocol variants based on ISO 9798-2 (challenge-response with encrypted response) and ISO 10181-2 (challenge-response with encrypted challenge), and with respect to different adversary models. The authenticity proof of the ISO 9798-2 variant leads to an improvement considering mechanism embedment which also provides availability if certain fairness conditions on the adversary behavior hold.

(ii) The purpose of the case study is to show the usefulness of a methodology for the formal development of secure systems in Focus which is based on explicit adversary modelling and defining security as a relation between two system specifications describing ordinary system behaviour and attack situations. The methodology is based on pulse-driven stream-processing function semantics. The case study shows the use of different Focus specification styles and formats on the design level. When introducing the security protocols, a functional specification can be immediately achieved from the informal description given in the ISO standards documents. The functional specification turns out to be cumbersome with respect to security proofs; thus, a relational variant is given. The functional specification is shown to refine the relational one, which itself refines the original system specification, showing that the introduction of the security mechanisms does not violate the intended system behavior. Based on the relational specification, authenticity properties are proved with respect to a simple and a more complex adversary model. With the authenticity proof,

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it turns out that due to the buffering of incoming challenges, the system is blocked in case of an attack, with availability being lost. In order to achieve availability, the timing of challenges has to be considered, leading to a time dependent protocol specification that is available with respect to some fairness assumptions and preserves authenticity.

(iii) (a) Volkmar Lotz.
Threat Scenarios as a Means to Formally Develop Secure Systems.
in: E. Bertino, H. Kurth, G. Martella, E. Montolivo (editors), "Computer Security – ESORICS '96", LNCS 1146, Springer, 1996.

(b) Volkmar Lotz.
Threat Scenarios as a Means to Formally Develop Secure Systems.
Technical Report TUM-I9709, Technische Universität München, 1997.

2.35 A Traffic Light Controller

(i) Starting from an informal textual description, a traffic light controller of a simple two-way intersection is specified by a network of data flow components. The main focus of this work is in the use of readable description techniques, such as state transition diagrams and tables, to hide the mathematical background of the specifications. A second main point is the use of automated formal verification tools to prove properties of the specification.

(ii) The specification consists of three components that communicate via synchronous streams, where in each time interval exactly one message is transmitted. Two components handle the low-level interfaces to the inductive loop sensors and the signal lamps. They are specified as a relation between input and output streams, and as a small functional program, respectively. The third component is the controller itself. It is modeled as a state transition diagram, where the individual transitions are described with tables. This makes the specification comparatively compact. The specification is validated by translating it to a program in the input language of the model checker SMV, and verifying formulas in the temporal logic CTL that describe certain aspect of the expected controller behavior.

(iii) Jan Philipps, Alexander Schmidt.
Traffic Flow by Data Flow.
SFB-Report 342/13/97A, Technische Universität München, 1997.

2.36 The TimeWarp Simulator

(i) The Time Warp-mechanism accomplishes an efficient synchronisation between the components of a distributed discrete event-driven simulator. According to this mechanism the components of the simulator calculate locally while sending results to other components without waiting for any events to be received, with the optimistic hope that causality is preserved. In case of a miscalculation recognized by messages received too late, the components have to perform

a rollback and cancel some messages already sent, maybe causing other components also to rollback. Nevertheless the distributed Time Warp-algorithm returns a correct result. In this case study this technique is modelled with the development method FOCUS and the correctness is formally investigated. Starting from a simple, centralized simulator three development steps are performed, reaching a distributed simulator using Time Warp. The simulators on various abstraction levels are specified formally and the development steps are verified using the techniques of FOCUS.

(ii) In this case study, a distributed simulator was developed through four different abstraction layers. The validity of the refinements (including behaviour, structural and interface refinement) is proved formally, showing that the communication mechanism of the Time Warp is correct if the algorithm for the calculation of the global virtual time is asumed to be correct.

An extensive multiple use of similar components occures, making the need for appropriate notions obvious. A suggestion for tabular specifications was given that uses a variable number of communication channels, described by parametrized columns. Also a tabular form for describing the communication behaviour for synchronous communication was suggested. Some ideas that could lead to an improved support for the treatment of refinement relations can be found in the detailed proofs, e.g. proofs using the states of systems, using equivalence classes of the streams and using the concept of refinement mappings.

(iii) Max Breitling.
Specifying and Verifying TIMEWARP *with* FOCUS.
To appear, Technische Universität München, 1997.

2.37   The Steam Boiler

(i) This paper demonstrates how to carry out the specification of a steam boiler and its controller in FOCUS. We do not give a complete detailed specification, but concentrate on the conceptual formal model in FOCUS for the control task starting from classical control theory. We apply FOCUS to the development of the requirement specification of a steam boiler controller to demonstrate some of its strengths: its expressiveness and adaptability in different domains – here the adaptation to control theory –, its user-friendly extensions – here the table notation technique – and the broad range of refinement techniques – here the concept of data and state refinement.

(ii) In the first part of the article the terms and the functional model of control theory are mapped on the semantic model of stream processsing functions. A network scheme is introduced capturing the basic notions of control theory. In the second part the notion of structured specifications using conceptual states and a special form of tabular representation for the description of these system requirements is introduced. Again, stream processing functions are used as the

formal model; thus, tables are interpreted as short-hand notations for predicates over stream processing functions and with states as additional parameter. To demonstrate the possibilities and restrictions of such an approach, several requirements of the steam boiler are represented in tabular fashion.

Finally, refinement is discussed and embedded in the tabular approach and demonstrated in the steam boiler context.

(iii) M. Broy, F. Regensburger, B. Schätz, K. Spies.
Streams of Steam.
SFB-Report 342/14/97A, Technische Universität München, 1997.

Acknowledgements

SFB 342:   Methoden und Werkzeuge für die Nutzung paralleler Rechnerarchitekturen

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Reihe A

**Liste aller erschienenen Berichte von 1990-1994
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