An Improvement of McMillan’s Unfolding Algorithm

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1 Introduction

In a seminal paper [8], McMillan has proposed a new technique to avoid the state explosion problem in the verification of systems modelled with finite-state Petri nets. The technique is based on the concept of net unfolding, a well known partial order semantics of Petri nets introduced in [10], and later described in more detail in [3] under the name of branching processes. The unfolding of a net is another net, usually infinite but with a simpler structure. McMillan proposes an algorithm for the construction of a finite initial part of the unfolding which contains full information about the reachable states. We call such an initial part a finite complete prefix. He then shows how to use these prefixes for deadlock detection.

The unfolding technique has been later applied to other verification problems. In [6, 7] it is used to check relevant properties of speed independent circuits. In [4], an unfolding-based model checking algorithm for a simple branching time logic is proposed. Recently, the technique has also been used for the verification of timed systems [9].

Although McMillan’s algorithm is simple and elegant, it sometimes generates prefixes much larger than necessary. In some cases a minimal complete prefix has $O(n)$ in the size of the Petri net, while the algorithm generates a prefix of size $O(2^n)$. In this paper we provide an algorithm which generates a minimal complete prefix (in a certain sense to be defined). The prefix is always smaller than or as large as the prefix generated with the old algorithm.

The paper is organised as follows. Section 2 contains basic definitions about Petri nets and branching processes. In Section 3 we show that McMillan’s algorithm is just an element of a whole family of algorithms for the construction of finite complete prefixes. In Section 4 we select an element of this family, and show that it generates minimal prefixes in a certain sense. Finally, in Section 5 we present experimental results.

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2 Basic definitions

2.1 Petri nets

A triple \((S, T, F)\) is a net if \(S \cap T = \emptyset\) and \(F \subseteq (S \times T) \cup (T \times S)\). The elements of \(S\) are called places, and the elements of \(T\) transitions. Places and transitions are generically called nodes. We identify \(F\) with its characteristic function on the set \((S \times T) \cup (T \times S)\).

The preset of a node \(x\), denoted by \(\bullet x\), is the set \(\{y \in S \cup T \mid F(y, x) = 1\}\). The postset of \(x\), denoted by \(x^*\), is the set \(\{y \in S \cup T \mid F(x, y) = 1\}\).

A marking of a net \((S, T, F)\) is a mapping \(S \rightarrow \mathbb{N}\). A 4-tuple \(\Sigma = (S, T, F, M_0)\) is a net system if \((S, T, F)\) is a net and \(M_0\) is a marking of \((S, T, F)\) (called the initial marking of \(\Sigma\)). A marking \(M\) enables a transition \(t\) if \(\forall s \in S: F(s, t) \leq M(s)\). If \(t\) is enabled at \(M\), then it can occur, and its occurrence leads to a new marking \(M'\) (denoted \(M \xrightarrow{t} M'\)), defined by \(M'(s) = M(s) - F(s, t) + F(t, s)\) for every place \(s\). A sequence of transitions \(\sigma = t_1 t_2 \ldots t_n\) is an occurrence sequence if there exist markings \(M_1, M_2, \ldots, M_n\) such that

\[
M_0 \xrightarrow{t_1} M_1 \xrightarrow{t_2} \ldots M_{n-1} \xrightarrow{t_n} M_n
\]

\(M_n\) is the marking reached by the occurrence of \(\sigma\), also denoted by \(M_0 \xrightarrow{\sigma} M_n\). \(M\) is a reachable marking if there exists an occurrence sequence \(\sigma\) such that \(M_0 \xrightarrow{\sigma} M\).

A marking \(M\) of a net is \(n\)-safe if \(M(s) \leq n\) for every place \(s\). We identify \(1\)-safe markings with the set of places \(s\) such that \(M(s) = 1\). A net system \(\Sigma\) is \(n\)-safe if all its reachable markings are \(n\)-safe.

In this paper we consider only net systems satisfying the following two additional properties:

- The number of places and transitions is finite.
- Every transition of \(T\) has a nonempty preset and a nonempty postset.

2.2 Branching processes

Branching processes are “unfoldings” of net systems containing information about both concurrency and conflicts. They were introduced by Engelfriet in [3]. We quickly review the main definitions and results of [3].

Occurrence nets. Let \((S, T, F)\) be a net and let \(x_1, x_2 \in S \cup T\). The nodes \(x_1\) and \(x_2\) are in conflict, denoted by \(x_1 \# x_2\), if there exist distinct transitions \(t_1, t_2 \in T\) such that \(\bullet t_1 \cap \bullet t_2 \neq \emptyset\), and \((t_1, x_1), (t_2, x_2)\) belong to the reflexive and transitive closure of \(F\). In other words, \(x_1\) and \(x_2\) are in conflict if there exist two paths leading to \(x_1\) and \(x_2\) which start at the same place and immediately diverge (although later on they can converge again). For \(x \in S \cup T\), \(x\) is in self-conflict if \(x \# x\).

An occurrence net is a net \(N = (B, E, F)\) such that:

- for every \(b \in B\), \(|b| \leq 1\),
- \(F\) is acyclic, i.e. the (irreflexive) transitive closure of \(F\) is a partial order,
In other words, a homomorphism is a mapping that preserves the nature of nodes and branching processes of the environment of transitions.

The elements of $B$ and $E$ are called conditions and events, respectively. $\text{Min}(N)$ denotes the set of minimal elements of $B \cup E$ with respect to the transitive closure of $F$. The (irreflexive) transitive closure of $F$ is called the causal relation, and denoted by $\prec$. The symbol $\leq$ denotes the reflexive and transitive closure of $F$. Given two nodes $x, y \in B \cup E$, we say $x \Rightarrow y$ if neither $x < y$ nor $y < x$ nor $x \# y$.

**Branching processes.** Let $N_1 = (S_1, T_1, F_1)$ and $N_2 = (S_2, T_2, F_2)$ be two nets. A homomorphism from $N_1$ to $N_2$ is a mapping $h: S_1 \cup T_1 \rightarrow S_2 \cup T_2$ such that:

- $h(S_1) \subseteq S_2$ and $h(T_1) \subseteq T_2$, and
- for every $t \in T_1$, the restriction of $h$ to $\bullet t$ is a bijection between $\bullet t$ (in $N_1$) and $\bullet h(t)$ (in $N_2$), and similarly for $t^\bullet$ and $h(t)^\bullet$.

In other words, a homomorphism is a mapping that preserves the nature of nodes and the environment of transitions.

A branching process of a net system $\Sigma = (N, M_0)$ is a pair $\beta = (N', p)$ where $N' = (B, E, F)$ is an occurrence net, and $p$ is a homomorphism from $N'$ to $N$ such that

- (i) The restriction of $p$ to $\text{Min}(N')$ is a bijection between $\text{Min}(N')$ and $M_0$,
- (ii) for every $e_1, e_2 \in E$, if $\bullet e_1 = \bullet e_2$ and $p(e_1) = p(e_2)$ then $e_1 = e_2$.

Figure 1 shows a 1-safe net system (part (a)), and two of its branching processes (parts (b) and (c)).

Two branching processes $\beta_1 = (N_1, p_1)$ and $\beta_2 = (N_2, p_2)$ of a net system are isomorphic if there is a bijective homomorphism $h$ from $N_1$ to $N_2$ such that $p_2 \circ h = p_1$. Intuitively, two isomorphic branching processes differ only in the names of conditions and events.

It is shown in [3] that a net system has a unique maximal branching process up to isomorphism. We call it the unfolding of the system. The unfolding of the 1-safe system of Figure 1 is infinite.

Let $\beta' = (N', p')$ and $\beta = (N, p)$ be two branching processes of a net system. $\beta'$ is a prefix of $\beta$ if $N'$ is a subnet of $N$ satisfying

- $\text{Min}(N)$ belongs to $N'$,
- if a condition belongs to $N'$, then its input event in $N$ also belongs to $N'$, and
- if an event belongs to $N'$, then its input and output conditions in $N$ also belong to $N'$.

and $p'$ is the restriction of $p$ to $N'$.

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1 In [3], homomorphisms are defined between net systems, instead of between nets, but this is only a small technical difference without any severe consequence.
2.3 Configurations and cuts

A configuration $C$ of an occurrence net is a set of events satisfying the following two conditions:

- $e \in C \Rightarrow \forall e' \leq e : e' \in C$ (C is causally closed).
- $\forall e, e' \in C : \neg(e \# e')$ (C is conflict-free).

A set $B'$ of conditions of an occurrence net is a co-set if its elements are pairwise in co relation. A maximal co-set $B'$ with respect to set inclusion is called a cut.

A marking $M$ of a system $\beta$ is represented in a branching process $\beta = (N, p)$ of $\Sigma$ if $\beta$ contains a cut $c$ such that, for each place $s$ of $\Sigma$, $c$ contains exactly $M(s)$ conditions $b$ with $p(b) = s$. It is easy to prove using results of [1, 3] that every marking represented in a branching process is reachable, and that every reachable marking is represented in the unfolding of the net system.

Finite configurations and cuts are tightly related. Let $C$ be a finite configuration of a branching process $\beta = (N, p)$. Then the co-set $\text{Cut}(C)$, defined below, is a cut:

$$\text{Cut}(C) = (\text{Min}(N) \cup C^*) \setminus C.$$  

In particular, given a configuration $C$ the set of places $p(\text{Cut}(C))$ is a reachable marking, which we denote by $\text{Mark}(C)$.

For 1-safe systems, we have the following result, which will be later used in Section 4:

**Proposition 2.1**

Let $x_1$ and $x_2$ be two nodes of a branching process of a 1-safe net system. If $x_1 \equiv x_2$, then $p(x_1) \neq p(x_2)$.

Given a cut $c$ of a branching process $\beta = (N, p)$, we define $\uparrow c$ as the pair $(N', p')$, where $N'$ is the unique subnet of $N$ whose set of nodes is $\{x \mid (\exists y \in c : x \geq y) \land \forall y \in c : \neg(x \# y)\}$ and $p'$ is the restriction of $p$ to the nodes of $N$. The following result will also be used later:
Proposition 2.2

If $\beta$ is a branching process of $(N, M_0)$ and $c$ is a cut of $\beta$, then $\uparrow c$ is a branching process of $(N, p(c))$. \hfill 2.2

3 An algorithm for the construction of a complete finite prefix

3.1 Constructing the unfolding

We give an algorithm for the construction of the unfolding of a net system. First of all, let us describe a suitable data structure for the representation of branching processes.

We implement a branching process of a net system $\Sigma$ as a list $n_1, \ldots, n_k$ of nodes. A node is either a condition or an event. A condition is a pair $(s, e)$, where $s$ is a place of $\Sigma$ and $e$ the input event. An event is a pair $(t, B)$, where $t$ is a transition of $\Sigma$, and $B$ is the set of input conditions. Notice that the flow relation and the labelling function of a branching process are already encoded in its list of nodes. How to express the notions of causal relation, configuration or cut in terms of this data structure is left to the reader.

The algorithm for the construction of the unfolding starts with the branching process having the conditions corresponding to the initial marking of $\Sigma$ and no events. Events are added one at a time together with their output conditions.

We need the notion of “events that can be added to a given branching process”.

Definition 3.1

Let $\beta = n_1, \ldots, n_k$ be a branching process of a net system $\Sigma$. The possible extensions of $\beta$ are the pairs $(t, B)$, where $B$ is a co-set of conditions of $\beta$ and $t$ is a transition of $\Sigma$ such that

- $p(B) = \cdot t$, and
- $\beta$ contains no event $e$ satisfying $p(e) = t$ and $\cdot e = B$

$PE(\beta)$ denotes the set of possible extensions of $\beta$. \hfill 3.1

Algorithm 3.2 The unfolding algorithm

input: A net system $\Sigma = (N, M_0)$, where $M_0 = \{s_1, \ldots, s_n\}$.
output: The unfolding $Unf$ of $\Sigma$.
begin
$Unf := (s_1, \emptyset), \ldots, (s_n, \emptyset)$;
$pe := PE(Unf)$;
while $pe \neq \emptyset$ do
    append to $Unf$ an event $e = (t, B)$ of $pe$ and a condition $(s, e)$ for every output place $s$ of $t$;
    $pe := PE(Unf)$
endwhile
end \hfill 3.2
The algorithm does not necessarily terminate. In fact, it terminates if and only if the input system $\Sigma$ does not have any infinite occurrence sequence. It is correct only under the fairness assumption that every event added to $\mathcal{P}e$ is eventually chosen to extend $Unf$ (the correctness proof follows easily from the definitions and from the results of [3]).

**Constructing a finite complete prefix**

We say that a branching process $\beta$ of a net system $\Sigma$ is complete if the following two conditions hold:

- every reachable marking of $\Sigma$ is represented in $\beta$, and
- if a transition $t$ can occur in $\Sigma$, then $\beta$ contains an event labelled by $t$.

The unfolding of a net system is always complete. It is also easy to see that a complete prefix contains as much information as the unfolding.

Since an $n$-safe net system has only finitely many reachable markings, its unfolding contains at least one complete finite prefix. We show how to transform the algorithm above into a new one whose output is a finite complete prefix.

We need some preliminary notations and definitions:

Given a configuration $C$, we denote by $C \oplus E$ the fact that $C \cup E$ is a configuration such that $C \cap E = \emptyset$. We say that $C \oplus E$ is an extension of $C$, and that $E$ is a suffix of $C$.

Let $C_1$ and $C_2$ be two finite configurations such that $Mark(C_1) = Mark(C_2)$. It follows easily from the definitions that $\uparrow Cut(C_i)$ is isomorphic to the unfolding of $\Sigma' = (N, Mark(C_i))$, $i = 1, 2$; hence, $\uparrow Cut(C_1)$ and $\uparrow Cut(C_2)$ are isomorphic. Moreover, there is an isomorphism $I_{C_1}^{C_2}$ from $\uparrow Cut(C_1)$ to $\uparrow Cut(C_2)$. This isomorphism induces a mapping from the finite extensions of $C_1$ onto the extensions of $C_2$: it maps $C_1 \oplus E$ onto $C_2 \oplus I_{C_1}^{C_2}(E)$.

We can now introduce the three basic notions of the algorithm:

**Definition 3.3**

A partial order $\prec$ on the finite configurations of a branching process is an adequate order if:

- $\prec$ is well-founded,
- $\prec$ refines $\subseteq$, i.e. $C_1 \subset C_2$ implies $C_1 \prec C_2$, and
- $\prec$ is preserved by finite extensions, meaning that if $C_1 \prec C_2$ and $Mark(C_1) = Mark(C_2)$, then $C_1 \oplus E \prec C_2 \oplus I_{C_1}^{C_2}(E)$.

**Definition 3.4** *Local configuration*

The local configuration $[e]$ of an event of a branching process is the set of events $e'$ such that $e' \leq e$.

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\(^2\)It is immediate to prove that $[e]$ is a configuration.
Definition 3.5 *Cut-off event*

Let $\beta$ be a branching process and let $\prec$ be an adequate partial order on the configurations of $\beta$. An event $e$ is a *cut-off event* (with respect to $\prec$) if $\beta$ contains a local configuration $[e']$ such that

(a) $\text{Mark}([e]) = \text{Mark}([e'])$, and 
(b) $[e'] \prec [e]$.

The new algorithm has as parameter an adequate order $\prec$, i.e. every different adequate order leads to a different algorithm.

Algorithm 3.6 *The complete finite prefix algorithm*

**input:** An $n$-safe net system $\Sigma = (N, M_0)$, where $M_0 = \{s_1, \ldots, s_k\}$.

**output:** A complete finite prefix $\text{Fin}$ of $\text{Unf}$.

begin 
$\text{Fin} := (s_1, \emptyset), \ldots, (s_k, \emptyset)$;
$\text{pe} := \text{PE}(\text{Fin})$;
$\text{cut-off} := \emptyset$;

while $\text{pe} \neq \emptyset$ do 
choose an event $e = (t, B)$ in $\text{pe}$ such that $[e]$ is minimal with respect to $\prec$;
if $[e] \cap \text{cut-off} = \emptyset$ then
append to $\text{Fin}$ the event $e$ and a condition
$(s, e)$ for every output place $s$ of $t$;
$\text{pe} := \text{PE}(\text{Fin})$;
if $e$ is a cut-off event of $\text{Fin}$ then
$\text{cut-off} := \text{cut-off} \cup \{e\}$
endif
else $\text{pe} := \text{pe} \setminus \{e\}$
endif
endwhile
end

McMillan’s algorithm in [8] corresponds to the order

$$C_1 \prec_m C_2 :\iff |C_1| < |C_2|.$$ 

It is easy to see that $\prec_m$ is adequate.

The reason of condition (a) in the definition of cut-off event is intuitively clear in the light of this algorithm. Since $\text{Mark}([e']) = \text{Mark}([e])$, the continuations of $\text{Unf}$ from $\text{Cut}([e])$ and $\text{Cut}([e'])$ are isomorphic. Then, loosely speaking, all the reachable markings that we find in the continuation of $\text{Unf}$ from $\text{Cut}([e])$ are already present in the continuation from $\text{Cut}([e'])$, and so there is no need to have the former in $\text{Fin}$. The role of condition
Figure 2: A 1-safe net system

Figure 3: A prefix of the net system of Figure 2

(b) is more technical. In fact, when McMillan’s algorithm is applied to “ordinary” small examples, condition (b) seems to be superfluous, and the following strategy seems to work: if an event \( e \) is added and \( FIn \) already contains a local configuration \([e']\) such that \( \text{Mark}([e]) = \text{Mark}([e'])\), then mark \( e \) as cut-off event. The following example (also independently found by K. McMillan) shows that this strategy is incorrect. Consider the 1-safe net system of Figure 2.

The marking \( \{s_{12}\} \) is reachable. Without condition (b) we can generate the prefix of Figure 3.

The names of the events are numbers which indicate the order in which they are added to the prefix. The events 8 and 10 are cut-off events, because their corresponding markings \( \{s_7, s_9, s_{10}\} \) and \( \{s_6, s_8, s_{11}\} \) are also the markings corresponding to the events 7 and 9, respectively. This prefix is not complete, because \( \{s_{12}\} \) is not represented in it.

We now prove the correctness of Algorithm 3.6.
Proposition 3.7

\( \text{Fin is finite.} \)

**Proof:** Given an event \( e \) of \( \text{Fin} \), define the *depth* of \( e \) as the length of a longest chain of events \( e_1 < e_2 < \ldots < e \); the depth of \( e \) is denoted by \( d(e) \). We prove the following two results:

1. For every event \( e \) of \( \text{Fin} \), \( d(e) \leq n + 1 \), where \( n \) is the number of reachable markings of \( \Sigma \).
   
   Since cuts correspond to reachable markings, every chain of events \( e_1 < e_2 < \ldots < e_n < e_{n+1} \) of \( \text{Unf} \) contains two events \( e_i, e_j, i < j \), such that \( \text{Mark}(\{e_i\}) = \text{Mark}(\{e_j\}) \). Since \( [e_i] \subset [e_j] \) and \( \prec \) refines \( \subset \), we have \( [e_i] \prec [e_j] \), and therefore \( [e_j] \) is a cut-off event of \( \text{Unf} \). Should the finite prefix algorithm generate \( e_j \), then it has generated \( e_i \) before and \( e_j \) is recognized as a cut-off event of \( \text{Fin} \), too.

2. For every event \( e \) of \( \text{Fin} \), the sets \( \cdot e \) and \( e^* \) are finite.
   
   By the definition of homomorphism, there is a bijection between \( p(e)^* \) and \( p(e^*) \), where \( p \) denotes the homomorphism of \( \text{Fin} \), and similarly for \( \cdot p(e) \) and \( p(\cdot e) \). The result follows from the finiteness of \( N \).

3. For every \( k \geq 0 \), \( \text{Fin} \) contains only finitely many events \( e \) such that \( d(e) \leq k \).
   
   By complete induction on \( k \). The base case, \( k = 0 \), is trivial. Let \( E_k \) be the set of events of depth at most \( k \). We prove that if \( E_k \) is finite then \( E_{k+1} \) is finite.
   
   By (2) and the induction hypothesis, \( E_k^* \) is finite. Since \( \cdot E_{k+1} \subset E_k^* \cup \text{Min}(\text{Fin}) \), we get by property (ii) in the definition of a branching process that \( E_{k+1} \) is finite.

It follows from (1) and (3) that \( \text{Fin} \) only contains finitely many events. By (2) it contains only finitely many conditions. \( \blacksquare \)

Proposition 3.8

\( \text{Fin is complete.} \)

**Proof:** (a) Every reachable marking of \( \Sigma \) is represented in \( \text{Fin} \).

Let \( M \) be an arbitrary reachable marking of \( \Sigma \). There exists a configuration \( C \) of \( \text{Unf} \) such that \( \text{Mark}(C) = M \). If \( C \) is not a configuration of \( \text{Fin} \), then it contains some cut-off event \( e \), and so \( C = [e] \oplus E \) for some set of events \( E \). By the definition of a cut-off event, there exists a local configuration \( [e'] \) such that \( [e'] \prec [e] \) and \( \text{Mark}([e']) = \text{Mark}([e]) \).

Consider the configuration \( C' = [e'] \oplus I^e_{[e]}(E) \). Since \( \prec \) is preserved by finite extensions, we have \( C' \prec C \). Moreover, \( \text{Mark}(C') = M \). If \( C' \) is not a configuration of \( \text{Fin} \), then we can iterate the procedure and find a configuration \( C'' \) such that
$C'' \prec C'$ and $\text{Mark}(C'') = M$. The procedure cannot be iterated infinitely often because $\prec$ is well-founded. Therefore, it terminates in a configuration of $\text{Fin}$.

(b) If a transition $t$ can occur in $\Sigma$, then $\text{Fin}$ contains an event labelled by $t$. If $t$ occurs in $\Sigma$, then some reachable marking $M$ enables $t$. The marking $M$ is represented in $\text{Fin}$. Let $C$ be a minimal configuration with respect to $\prec$ such that $\text{Mark}(\text{Cut}(C)) = M$. If $C$ contains some cut-off event, then we can apply the arguments of (a) to conclude that $\text{Fin}$ contains a configuration $C' \prec C$ such that $\text{Mark}(\text{Cut}(C')) = M$. This contradicts the minimality of $C$. So $C$ contains no cut-off events, and therefore $\text{Fin}$ also contains a configuration $C \oplus \{e\}$ such that $e$ is labelled by $t$.

\section{An adequate order for the 1-safe case}

As we mentioned in the introduction, McMillan’s algorithm may be inefficient in some cases. An extreme example, due to Kishinevsky and Taubin, is the family of systems on the left of Figure 4.

While a minimal complete prefix has size $O(n)$ in the size of the system (see the dotted line in Figure 4), the branching process generated by McMillan’s algorithm has size $O(2^n)$. The reason is that, for every marking $M$, all the local configurations $[e]$ satisfying $\text{Mark}([e]) = M$ have the same size, and therefore there exist no cut-off events with respect to McMillan’s order $\prec_m$.

Our parametric presentation of Algorithm 3.6 suggests how to improve this; it suffices to find a new adequate order $\prec_r$ that refines McMillan’s order $\prec_m$. Such an order induces a weaker notion of cut-off event; more precisely, every cut-off event with respect to $\prec_m$ is also a cut-off event with respect to $\prec_r$, but maybe not the other way round. Therefore, the instance of Algorithm 3.6 which uses the new order generates at least as many cut-off events as McMillan’s instance, and maybe more. In the latter case, Algorithm 3.6 generates a smaller prefix.

The order $\prec_r$ is particularly good if in addition it is \textit{total}. In this case, whenever an
event \( e \) is generated after some other event \( e' \) such that \( \text{Mark}(\{e\}) = \text{Mark}(\{e'\}) \), we have \( [e'] \prec_r [e] \) (because events are generated in accordance with the total order \( \prec_r \)), and so \( e \) is marked as a cut-off event. So we have the following two properties:

- the guard “\( e \) is a cut-off event of \( \text{Fin} \)” in the inner if instruction of Algorithm 3.6 can be replaced by “\( \text{Fin} \) contains a local configuration \( [e'] \) such that \( \text{Mark}([e]) = \text{Mark}([e']) \)”, and
- the number of events of the complete prefix which are not cut-off events cannot exceed the number of reachable markings.

In the sequel, let \( \Sigma = (N, M_0) \) be a fixed net system, and let \( \ll \) be an arbitrary total order on the transitions of \( \Sigma \). We extend \( \ll \) to a partial order on sets of events of a branching process: for such a set \( E \), let \( \varphi(E) \) be that sequence of transitions which is ordered according to \( \ll \) and contains each transition \( t \) as often as there are events in \( E \) with label \( t \); \( \varphi(E) \) is something like the Parikh-vector of \( E \). Now we say that \( E_1 \ll E_2 \) if \( \varphi(E_1) \) is shorter than \( \varphi(E_2) \), or if they have the same length but \( \varphi(E_1) \) is lexicographically smaller than \( \varphi(E_2) \). Note that \( E_1 \) and \( E_2 \) are incomparable with respect to \( \ll \) iff \( \varphi(E_1) = \varphi(E_2) \) and, in particular, \( |E_1| = |E_2| \).

We now define \( \prec_r \) more generally on suffixes of configurations of a branching process (recall that a set of events \( E \) is a suffix of a configuration if there exists a configuration \( C \) such that \( C \oplus E \)).

**Definition 4.1** Total order \( \prec_r \)

Let \( E_1 \) and \( E_2 \) be two suffixes of configurations of a branching process \( \beta \) and let \( \text{Min}(E_1) \) and \( \text{Min}(E_2) \) denote the sets of minimal elements of \( E_1 \) and \( E_2 \) with respect to the causal relation. We say \( E_1 \prec_r E_2 \) if:

- \( E_1 \ll E_2 \), or
- \( \varphi(E_1) = \varphi(E_2) \) and
  - \( \text{Min}(E_1) \ll \text{Min}(E_2) \), or
  - \( \varphi(\text{Min}(E_1)) = \varphi(\text{Min}(E_2)) \) and \( E_1 \setminus \text{Min}(E_1) \prec_r E_2 \setminus \text{Min}(E_2) \).

The second condition of this definition could be expressed as: the Foata-Normal-Form of \( E_1 \) is smaller than that of \( E_2 \) with respect to \( \ll \), cf. e.g. [2].

**Theorem 4.2**

Let \( \beta \) be a branching process of a 1-safe net system. \( \prec_r \) is an adequate total order on the configurations of \( \beta \).

**Proof:**

a) \( \prec_r \) is a partial order.

It is easy to see by induction on \( |E| \) that \( \prec_r \) is irreflexive. Now assume \( E_1 \prec_r E_2 \prec_r E_3 \). Clearly, \( E_1 \prec_r E_3 \) unless \( \varphi(E_1) = \varphi(E_2) = \varphi(E_3) \), which in particular implies
The base case gives \( |E_1| = |E_2| = |E_3| \). For such triples with these equalities we apply induction on the size: if \( \text{Min}(E_1) \leq \text{Min}(E_2) \) or \( \text{Min}(E_2) \leq \text{Min}(E_3) \), we conclude \( E_1 \prec_r E_3 \), and otherwise we apply induction to \( E_i \setminus \text{Min}(E_i) \), \( i = 1, 2, 3 \), which are also suffixes of configurations.

b) \( \prec_r \) is total on configurations.

Assume that \( C_1 \) and \( C_2 \) are two incomparable configurations, i.e. \( |C_1| = |C_2| \), \( \varphi(C_1) = \varphi(C_2) \), and \( \varphi(\text{Min}(C_1)) = \varphi(\text{Min}(C_2)) \). We prove \( C_1 = C_2 \) by induction on \( |C_1| = |C_2| \).

The base case gives \( C_1 = C_2 = \emptyset \), so assume \( |C_1| = |C_2| > 0 \).

We first prove \( \text{Min}(C_1) = \text{Min}(C_2) \). Assume without loss of generality that \( e_1 \in \text{Min}(C_1) \setminus \text{Min}(C_2) \). Since \( \varphi(\text{Min}(C_1)) = \varphi(\text{Min}(C_2)) \), \( \text{Min}(C_2) \) contains an event \( e_2 \) such that \( p(e_1) = p(e_2) \). Since \( \text{Cut}(\text{Min}(C_1)) = \text{Cut}(\text{Min}(C_2)) \), \( \text{Min}(N) \) carry different labels by Proposition 2.1, we have \( e_1 = e_2 \). This contradicts condition (ii) of the definition of branching process.

Since \( \text{Min}(C_1) = \text{Min}(C_2) \), both \( C_1 \setminus \text{Min}(C_1) \) and \( C_2 \setminus \text{Min}(C_2) \) are configurations of the branching process \( \uparrow \text{Cut}(\text{Min}(C_1)) \) of \( (N, \text{Mark}(\text{Min}(C_1))) \) (Proposition 2.2); by induction we conclude \( C_1 = C_2 \).

c) \( \prec_r \) is well-founded.

In a sequence \( C_1 \succ_r C_2 \succ_r \ldots \) the size of the \( C_i \) cannot decrease infinitely often; also, for configurations of the same size, \( C_i \) cannot decrease infinitely often with respect to \( \ll \), since the sequences \( \varphi(C_i) \) are drawn from a finite set; an analogous statement holds for \( \text{Min}(C_i) \). Hence, we assume that all \( |C_i| \), all \( \varphi(C_i) \) and all \( \varphi(\text{Min}(C_i)) \) are equal and apply induction on the common size. For \( |C_i| = 0 \), an infinite decreasing sequence is impossible. Otherwise, we conclude as in case b) that we would have \( C_1 \setminus \text{Min}(C_1) \succ_r C_2 \setminus \text{Min}(C_2) \succ_r \ldots \) in \( \uparrow \text{Cut}(\text{Min}(C_1)) \), which is impossible by induction.

d) \( \prec_r \) refines \( \subset \).

Obvious.

e) \( \prec_r \) is preserved by finite extensions.

This is the most intricate part of the proof, and here all the complications in Definition 4.1 come into play. Take \( C_1 \prec_r C_2 \) with \( \text{Mark}(C_1) = \text{Mark}(C_2) \). We have to show that \( C_1 \uplus E \prec_r C_2 \uplus I_{C_1}^E(e) \), and we can assume that \( E = \{e\} \) and apply induction afterwards. The case \( C_1 \ll C_2 \) is easy, hence assume \( \varphi(C_1) = \varphi(C_2) \), and in particular \( |C_1| = |C_2| \). We show first that \( e \) is minimal in \( C'_1 = C_1 \cup \{e\} \) if and only if \( I_{C_1}^E(e) \) is minimal in \( C'_2 = C_2 \cup \{I_{C_1}^E(e)\} \).

So let \( e \) be minimal in \( C'_1 \), i.e. the transition \( p(e) \) is enabled under the initial marking. Let \( s \in \text{Cut}(C_2) \) with label \( p(e) \) are minimal conditions of \( \beta \), and \( I_{C_1}^E(e) = e \) is minimal in \( C'_2 \). The
reverse implication holds analogously, since about $C_1$ and $C_2$ we have only used the hypothesis $\varphi(C_1) = \varphi(C_2)$.

With this knowledge about the positions of $e$ in $C'_1$ and $I_C'(e)$ in $C'_2$, we proceed as follows. If $\text{Min}(C_1) \ll \text{Min}(C_2)$, then we now see that $\text{Min}(C'_1) \ll \text{Min}(C'_2)$, so we are done. If $\varphi(\text{Min}(C_1)) = \varphi(\text{Min}(C_2))$ and $e \in \text{Min}(C'_1)$, then

$$C'_1 \setminus \text{Min}(C'_1) = C_1 \setminus \text{Min}(C_1) \prec_r C_2 \setminus \text{Min}(C_2) = C'_2 \setminus \text{Min}(C'_2)$$

hence $C'_1 \prec_r C'_2$. Finally, if $\varphi(\text{Min}(C_1)) = \varphi(\text{Min}(C_2))$ and $e \notin \text{Min}(C'_1)$, we again argue that $\text{Min}(C_1) = \text{Min}(C_2)$ and that, hence, $C_1 \setminus \text{Min}(C_1)$ and $C_2 \setminus \text{Min}(C_2)$ are configurations of the branching process $\uparrow \text{Cut}(\text{Min}(C_1))$ of $(N, \text{Mark}(\text{Min}(C_1)))$; with an inductive argument we get $C'_1 \setminus \text{Min}(C'_1) \prec_r C'_2 \setminus \text{Min}(C'_2)$ and are also done in this case. □

We close this section with a remark on the minimality of the prefixes generated by the new algorithm, i.e. by Algorithm 3.6 with $\prec_r$ as adequate order. Figure 1(b) and (c) are a minimal complete prefix and the prefix generated by the new algorithm for the 1-safe system of Figure 1(a), respectively. It follows that the new algorithm does not always compute a minimal complete prefix. However, the prefixes computed by the algorithm are minimal in another sense. The algorithm stores only the reachable markings corresponding to local configurations, which for the purpose of this discussion we call local markings. This is the feature which makes the algorithm interesting for concurrent systems: the local markings can be a very small subset of the reachable markings, and therefore the storage of the unfolding may require much less memory than the storage of the state space. In order to find out that the prefix of Figure 1(b) is complete, we also need to know that the initial marking $\{s_1, s_2\}$ appears again in the prefix as a non-local marking. If we only store information about local markings, then the prefix of Figure 1(c) is minimal, as well as all the prefixes generated by the new algorithm. The reason is the observation made above: all the local configurations of $\text{Fin}$ which are not induced by cut-off events correspond to different markings; therefore, in a prefix smaller than $\text{Fin}$ we lose information about the reachability of some marking.

5 Implementation issues and experimental results

The implementation of the Algorithm 3.6 has been carried out in the context of the model checker described in [4], which allows to efficiently verify formulae expressed in a simple branching time temporal logic.

For the storage of Petri nets and branching processes we have developed an efficient, universal data structure that allows fast access to single nodes [12]. This data structure is based on the underlying incidence matrix of the net. Places, transitions and arcs are represented by nodes of doubly linked lists to support fast insertion and deletion of single nodes.

The computation of new elements for the set $PE$ involves the combinatorial problem of finding sets of conditions $B$ such that $\varphi(B) = \epsilon t$ for some transition $t$. We have implemented several improvements in this combinatorial determination, which have significant influence on the performance of the algorithm. The interested reader is referred to [12].
Algorithm 3.6 is very simple, and can be easily proved correct, but is not efficient. In particular, it computes the set $PE$ of possible extensions each time a new event is added to $Fin$, which is clearly redundant. Similarly to McMillan’s original algorithm [8], in the implementation we use a queue to store the set $PE$ of possible extensions. The new events of $Fin$ are extracted from the head of this list, and, when an event is added, the new possible extensions it generates are appended to its tail.

The simplest way to organize the list would be to sort its events according to the total order $\prec_r$. However, this is again inefficient, because it involves performing unnecessary comparisons. The solution is to sort the events according to the size of their local configuration, as in [8], and compare events with respect to $\prec_r$ only when it is really needed.

With this implementation, the new algorithm only computes more than McMillan’s when two events $e$ and $e'$ satisfy $Mark([e]) = Mark([e'])$ and $|[e]| = |[e']|$. But this is precisely the case in which the algorithm behaves better, because it always identifies either $e$ or $e'$ as a cut-off event. In other words: when the complete prefix computed by McMillan’s algorithm is minimal, our algorithm computes the same result with no time overhead.

The running time of the new algorithm is $O((\frac{1}{\xi})^B)$, where $B$ is the set of conditions of the unfolding, and $\xi$ denotes the maximal size of the presets of the transitions in the original net (notice that this is not a measure in the size of the input). The dominating factor in the time complexity is the computation of the possible extensions. The space required is linear in the size of the unfolding because we store a finite amount of information per event.

Finally, we present some experimental results on two scalable examples. We compare McMillan’s algorithm and the new algorithm, both implemented using the universal data structure and the improvements in the combinatorial determination mentioned above.

The first example is a model of a concurrent $n$–buffer (see Figure 5(a)). The net has $2n$ places and $n+1$ transitions, where $n$ is the buffer’s capacity. While the number of reachable markings is $2^n$, $Fin$ has size $O(n^2)$ and contains one single cut-off event (see Table 1).
We have presented an algorithm for the computation of a complete finite prefix of an unfolding. We have used a refinement of McMillan’s basic notion of cut-off event. The prefixes constructed by the algorithm contain at most \( n \) non-cut-off events, where \( n \) is the number of reachable markings of the net. Therefore, we can guarantee that the prefix is never significantly larger than the reachability graph, what did not hold for the algorithm of [8].

### 6 Conclusions

We have presented an algorithm for the computation of a complete finite prefix of an unfolding. We have used a refinement of McMillan’s basic notion of cut-off event. The prefixes constructed by the algorithm contain at most \( n \) non-cut-off events, where \( n \) is the number of reachable markings of the net. Therefore, we can guarantee that the prefix is never significantly larger than the reachability graph, what did not hold for the algorithm of [8].

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3. All the times have been measured on a SPARCstation 20 with 48 MB main memory.

4. These times could not be calculated; for \( n = 7 \) we interrupted the computation after more than 12 hours.
References


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